

Amendments to the Claims

Claims 1-10 (**Canceled**)

Claim 11 (**Previously Presented**) A constant voltage generation device comprising:

- a reference voltage generation circuit operable to generate a reference voltage;
- an output circuit operable to generate an output voltage;
- a differential amplifier operable to generate a control signal based on the reference voltage and the output voltage; and
- a noise control circuit operable to remove short wave noises from the control signal to provide a second control signal, wherein
 - the output circuit is controlled in response to the second control signal so that the output voltage is constant, and
 - the noise control circuit comprises a resistor serially connected between an output terminal of the differential amplifier and an input terminal of the output circuit; and a capacitor connected between a second terminal and the output terminal of the differential amplifier.

Claim 12 (**Previously Presented**) The constant voltage generation device according to claim 11, wherein

- the second terminal is grounded.

Claim 13 (**Canceled**)

Claim 14 (**Previously Presented**) A constant voltage generation device comprising:

- a reference voltage generation circuit operable to generate a reference voltage;
- an output circuit operable to generate an output voltage;
- a differential amplifier operable to generate a control signal based on the reference voltage and the output voltage; and
- a first noise control circuit operable to remove short wave noises from the reference voltage; and

a second noise control circuit operable to remove short wave noises from the control signal to provide a second control signal, wherein

the output circuit is controlled in response to the second control signal so that the output voltage is constant,

the first noise control circuit comprises a first resistor serially connected between the reference voltage generation circuit and a first input terminal of the differential amplifier; and a first capacitor connected between a first terminal and the first input terminal of the differential amplifier, and

the second noise control circuit comprises a second resistor serially connected between an output terminal of the differential amplifier and an input terminal of the output circuit; and a second capacitor connected between a second terminal and the output terminal of the differential amplifier.

Claim 15 (Previously Presented) The constant voltage generation device according to claim 14, wherein

each of the first and second terminals is grounded.

Claim 16 (Previously Presented) A constant voltage generation device comprising:

a reference voltage generation circuit operable to generate a reference voltage;

an output circuit operable to generate an output voltage;

a differential amplifier operable to generate a control signal based on the reference voltage and the output voltage; and

a noise control circuit operable to extract short wave noises from the reference voltage, to be supplied to a first input terminal of the differential amplifier, and supply the extracted short wave noises into the output voltage, to be supplied to a second input terminal of the differential amplifier, wherein

the output circuit is controlled in level in response to the control signal so that the output voltage is constant.

Claim 17 (**Previously Presented**) The constant voltage generation device according to claim 16, wherein

the noise control circuit comprises a capacitor connected between the first input terminal and the second input terminal of the differential amplifier so that the short wave noises are cancelled.

Claim 18 (**Previously Presented**) The constant voltage generation device, according to claim 16, wherein

the noise control circuit is a high-pass filter connected between the first and second input terminals of the differential amplifier.

Claim 19 (**Previously Presented**) The constant voltage generation device according to claim 16, further comprising:

a second noise control circuit operable to remove short wave noises from the reference voltage.